a plurality of toroid transformers within said package by a soft silicone material, said toroid transformers each having wires wrapped thereon;

a plurality of terminal pins molded within and extending from the bottom of said package, each of said pins extending through a bottom portion of said side wall and below the inner portion of said side wall and having a notched solder post upon which said wires from said transformers are wrapped and soldered thereon, respectively;

said end walls having a first height H1 to form a standoff or safe guard between the surface of a printed circuit board and said terminal pins;

the outer portion of said side wall extending between said end walls and having a second height H2 which is less than said first height H1.

2. An electronic surface mount package for mounting onto the surface of a printed circuit board comprising:

a construction package having end walls, a side wall and an open bottom,

a plurality of toroid transformers within said package by a soft silicone material, said toroid transformers each having wires wrapped thereon,

a plurality of terminal pins molded within and extending from the bottom of said

package, each of said pins extending through a bottom portion of said side wall and below the

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inner portion of said side wall and having a notched solder post upon which said wires from said

transformers are wrapped and soldered thereon, respectively.

said end walls having a first height H1 to form a standoff or safe guard between the surface of a printed circuit board and said terminal pins;

the outer portion of said side wall extending between said end walls and having a second



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5. An electronic surface mount package for mounting onto the surface of a printed circuit board in an electronic device, said electronic surface mount package comprising:

a one piece construction package having end walls, a side wall and an open bottom,

a plurality of toroid transformers within said package by a soft silicone material, said toroid transformers each having wires wrapped thereon,

a plurality of terminal pins molded within and extending from the bottom of said package, each of said pins extending through a bottom portion of said side wall and below the inner portion of said side wall and having a notched solder post upon which said wires from said transformers are wrapped and soldered thereon, respectively,

said end walls having a first height H1 to form a standoff or safe guard between the surface of a printed circuit board and said terminal pins;

the outer portion of said side wall extending between said end walls and having a second height H2 which is less than said first height H1.

7. An electronic surface mount package for mounting onto the surface of a printed circuit board comprising:

a construction package having end walls, a side wall and an open bottom,

at least one toroid transformer within said package by a soft silicone material, said toroid transformer having a wire wrapped thereon,

at least one terminal pin molded within and extending from the bottom of said package, said pin extending through a bottom portion of said side wall and below the inner portion of said

side wall and having a notched solder post upon which said wire from said transformer is wrapped and soldered thereon,

said end walls having a first height H1 to form a standoff or safe guard between the surface of a printed circuit board and said terminal pins;

the outer portion of said side wall extending between said end walls and having a second height H2 which is less than said first height H1.

8. An electronic surface mount package for mounting onto the surface of a printed circuit board in an electronic device, said package comprising:

a one piece open construction package having end walls, side walls and an open bottom, a plurality of toroid transformers within said package, said toroid transformers each having a side wall and wires wound thereon,

a plurality of terminal pins molded within and extending through a bottom of said side wall and below the inner portion of said side wall, each of said pins having a hour-glass shaped notched solder post upon which said wires from said transformers are wrapped thereon, respectively,

said end walls having a first height H1 to form a standoff or safe guard between the surface of a printed circuit board and said terminal pins;

the outer portion of said side wall extending between said end walls and having a second height H2 which is less than said first height H1.

REMARKS

This Response is submitted in response to the Office Action mailed September 8, 2000.